

## EXHIBIT B

**Manware**

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**From:** Rawlins, Paul [Paul.Rawlins@COMPAQ.com]  
**Sent:** June 14, 2001 2:32 AM  
**To:** 'manware@fyvlaw.com'  
**Subject:** Efficient Snoop Filter draft comments

I finally got the opportunity to finish reading the patent disclosure on the plane this afternoon. Here are some comments that you can update the draft.

- ✓ Page 13: All references to the "response" module should be "request".
- ✓ Page 13 Line 4: Change to something like: "The request module 50 is responsible for maintaining proper cycle order. This is accomplished using content addressable memory (CAM) of new cycles against existing cycles."
- ✓ Page 14 Line 3: "illustrate" should be "illustrated".
- ✓ Page 14 Line 6: PCON does not necessarily filter the cycle requests to memory. It performs an address decode and sends the cycle to the appropriate destination (e.g. IO or memory).
- ✓ Page 14 Line 12: There are no "inverters" in the design. The diagram showed buffers intended to indicate locations in the designs where signals are buffered for fanout purposes. It has no logical impact.
- ✓ Page 15 Line 4: Remove "Because". Extend the sentence with: "since providing read requests as quickly as possible is advantageous. Write requests can generally be fulfilled with less time criticality."
- ✓ Page 16 Line 1: I couldn't locate figure 40.
- ✓ Page 16 Line 4: This also fulfills a requirement that writes are processed in order.
- ✓ Page 18 Line 22: Remove "but may be re-ordered if the request has a particular dependency". This implies that the cycle is reordered as it is added.
- ✓ Page 19 Line 5: The parent-child lists are exclusive to the read queues. This paragraph does not differentiate this.
- ✓ Page 19 Line 15: The use of "active cycle" is confusing in conjunction with the "Active List".
- ✓ Page 20 Line 1: Change to: "One clock cycle later, the status of the ASQ CAMHIT will be valid. If true, the abort status of the cycle will be set."
- ✓ Page 20 Line 4: Condition 2 only happens if the cycle was not aborted.

- ✓ Page 21 Line 6: "end" should be "and".
- ✓ Page 21 Line 9: Change "set by the user" to "BIOS" or "firmware".
- ✓ Page 22 Line 3: Indicate that the even and odd arbitration occurs independently..
- ✓ Page 24 Line 6: Instead of "clock cycle" use "transition". These transitions do not necessarily happen on successive clock cycles.

Let me know when you get the next draft complete.

Thanks,

Paul